


<b>CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)</b>			Docket No. <b>BUR920040109US1</b>
Applicant(s): <b>Anderson et al.</b>			
Application No. <b>10/710,909</b>	Filing Date <b>8/12/2004</b>	Examiner <b>Dang, Phuc T.</b>	Group Art Unit <b>2818</b>
Invention: <b>FinFETs (Fin Field Effect Transistors)</b>			
<div style="text-align: right;"><b>RECEIVED</b> <b>CENTRAL FAX CENTER</b> <b>MAR 24 2005</b></div>			
I hereby certify that this <u>Restriction Election (2 pages)</u> <small>(Identify type of correspondence)</small>			
is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. <u>703-872-9306</u> )			
on <u>3/24/2005</u> <small>(Date)</small>			
<div style="text-align: right;"><b>Kim Dwileski</b> <small>(Typed or Printed Name of Person Signing Certificate)</small>  <small>(Signature)</small></div>			
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P18/REV02

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MAR 24 2005

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**Applicant: Anderson *et al.*

Group Art Unit: 2818

Filed: 8/12/2004

Examiner: Dang, Phuc T.

Serial No.: 10/710,909

Title: FinFETs (Fin Field Effect Transistors)

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**RESTRICTION ELECTION**

In response to the Restriction Requirement dated March 2, 005, Applicants hereby provisionally elect Group I, claims 1-16, drawn to a method for fabricating a semiconductor structure, Class 438, subclass 281. This election is made with traverse, and Applicants hereby reserve the right to file a divisional application in connection with unelected claims 17-20, drawn to a semiconductor device.

With regard to the Restriction Requirement, Applicants respectfully submit that the subject matter of all claims 1-20 is sufficiently related that a thorough search for the subject matter of any one group of claims would encompass a search for the subject matter of the remaining claims. Thus, Applicants respectfully submit that the search and the examination of the entire application could be made without serious burden. See MPEP § 803, in which it is stated

10/710,909

that "if the search and examination of the entire application can be made without serious burden, the Examiner must examine it on the merits." Applicants respectfully submit that this policy should apply in the present application in order to avoid unnecessary delay and expense to Applicants and duplicative examination by the Patent Office.

Should the Examiner require or request anything further from Applicants prior to examination, the Examiner is requested to contact Applicants' undersigned representative at the telephone number below. Otherwise, Applicants request early and favorable examination on the merits.

Date: March 24, 2005



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10/710,909